S/N 09/135,413 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Examiner: Viet Q. Nguyen

Serial No.:

09/135,413

Group Art Unit: 2818

Filed:

August 14, 1998

Docket: 303.354US2

Title:

DEAPROM HAVING AMORPHOUS SILICON CARBIDE GATE

**INSULATOR** 

## COMMUNICATION CONCERNING COPENDING APPLICATIONS

WT: Assistant Commissioner for Patents Washington, D.C. 20231

Serial No.	Filing Date	Attorney Docket No.	Title
08/903,452	07/29/97	303.324US1	Transistor with Variable Electron Affinity Gate and Methods of Fabrication and Use
09/256,643	02/23/99	303.324US2	Transistor with Variable Electron Affinity Gate and Methods of Fabrication and Use
08/903,486	07/29/97	303.326US1	Silicon Carbide Gate Transistor and Fabrication Process
09/259,870	03/01/99	303.326US2	Silicon Carbide Gate Transistor and Fabrication Process
09/138,294	08/21/98	303.353US2	Transistor with Silicon Oxycarbide Gate and Methods of Fabrication and Use
08/902,843	07/29/97	303.354US1	DEAPROM Having Amorphous Silicon Carbide Gate Insulator
09/134,713	08/14/98	303.354US3	DEAPROM Having Amorphous Silicon Carbide Gate Insulator
08/902,098	07/29/97	303.355US1	DEAPROM and Transistor with Gallium Nitride or Gallium Aluminum Nitride Gate
09/140,978	08/27/98	303.355US2	DEAPROM and Transistor with Gallium Nitride or Gallium Aluminum Nitride Gate

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Serial Number: 09/135,413

Filing Date: August 14, 1998
Title: DEAPROM HAVING AMORPHOUS SILICON CARBIDE GATE INSULATOR

Serial No.	Filing Date	Attorney Docket No.	Title
09/141,392	08/27/98	303.355US3	DEAPROM and Transistor with Gallium Nitride or Gallium Aluminum Nitride Gate
08/902,133	07/29/97	303.356US1	Dynamic Electrically Alterable Programmable Read Only Memory Device
08/903,453	07/29/97	303.378US1	Carbonized Silicon Gate Insulators for Integrated Circuits
09/258,467	02/26/99	303.378US2	Carbonized Silicon Gate Insulators for Integrated Circuits

Respectfully submitted,

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